

Abstract

Tuning Circuit

A tuning circuit comprising a first reactance, a second reactance and a insulated gate
5 field effect transistor having a gate arranged to receive a control signal. The first
reactance is connected between the source of the field effect transistor and a first
node. The second reactance has the same value as the first reactance and is
connected between the drain of the field effect transistor and a second node. The
first and second nodes are arranged so as to experience a balanced ac signal.
10 Turning the field effect transistor on has the effect of making the first and second
reactances effective in the circuit and *vice versa*.